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Document Number (Continued)

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Applicant

David Meltzer

Filing Date

12/14/01

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2631

PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	PLACES DATE APPROXIMATE	
QG	5 382923	1/17/95	Shimada et al.	331	8	4/5/94	
QG	5 570042	10/29/96	Ma	326	63	1/3/95	
QG	5 581214	12/3/96	Iga	RECEIVED MAR 04 2002	31	8/30/95	
QG	5 793225	8/11/98	Gerson	Technology Center 2000	326	68	1/2/96
QG	5 905386	5/18/99	Gerson	326	73	2/27/98	
QG	6 246294	6/12/01	Gai	331	57	2/12/99	
QG	6 285262	2/9/01	Kuriyama	331	25	6/29/99	
QG	5 614843	3/25/97	Mita et al.	326	73	1/5/96	
QG	5 889437	3/30/99	Lee	331	16	7/11/97	
QG	6 111469	8/29/00	Adachi	331	17	8/20/98	
QG	4 736167	4/5/88	Kojima et al.	331	17	6/23/86	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (including Author, Title, Date, Patent No., Etc.)

QG	The TQ8103 Clock + Data Recovery (CDR) product description manufactured by Trigint Semiconductor Inc. of a monolithic CDR IC.
QG	The AMCC S3040A Device Specification (April 2, 2000) of a clock recovery unit.

EXAMINER

Spilka

DATE CONSIDERED

9/20/05

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